

Ser. No. 10/569,156  
Amdt. dated June 9, 2008  
Reply to Office action of March 25, 2008

PLU030268

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JUN 09 2008

Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of the Claims

1. (currently amended) An apparatus having a first mode of operation and a second mode of operation comprising:
  - a data bus;
  - a first power supply operating in said first mode, but not in said second mode;
  - a second power supply operating in said first mode and said second mode;
  - a third power supply operating in said first mode, but not in said second mode and said second mode; anda transistor with a base, collector and emitter wherein said ~~first~~third power supply is electrically coupled to the base, ~~the second~~said first power supply being electrically coupled to the collector, ~~the signal line~~said data bus being electrically coupled to the emitter and ~~the third~~said second power supply being electrically coupled to ~~the signal line~~said data bus;
  - wherein said transistor electrically isolates said data bus from said first power supply in said second mode of operation and electrically connects said data bus to said first power supply in ~~exhibits a conductive state during said first mode of operation and exhibits a non-conductive state during said second mode.~~
2. (currently amended) The apparatus of claim 1 wherein ~~the signal line~~ said data bus is connected to the ~~collector~~emitter via a resistor.
3. (currently amended) The apparatus of claim 2 wherein the resistor is located within a device subsystem
4. (currently amended) The apparatus of claim 3 wherein ~~the device~~said subsystem is an integrated circuit.

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5. (currently amended) An apparatus having a first mode of operation and a second mode of operation comprising:

a data bus;

a first power supply operating in said first mode, but not in said second mode;

a second power supply operating in said first mode and said second mode;

a switch-transistor, responsive to a first power supply voltage level, with a base, collector and emitter wherein said first power supply is electrically coupled to base and collector, said data bus line being electrically coupled to the emitter, and said second power supply being electrically coupled to said data bus;

wherein said switch-transistor electrically isolates a said data bus from a second said first power supply in a first said second mode of operation and electrically connects said data bus to said second-first power supply in a second said first mode of operation.

6. (canceled)

7. (currently amended) The apparatus of claim 5 wherein the data bus is connected to the switch-transistor via a resistor.

8. (currently amended) The apparatus of claim 7 wherein the resistor is located within a device subsystem.

9. (currently amended) The apparatus of claim 8 wherein the device said subsystem is an integrated circuit.

10. (canceled)

11. (currently amended) An apparatus having a first mode of operation and a second mode of operation comprising:

a data bus;

a power supply operating in said first mode and said second mode;

a control signal active in said first mode; and

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a switch-transistor, responsive to a said control signal, with a base, collector and emitter wherein said power supply is electrically coupled to said collector and said data bus, said control signal is electrically coupled to said base, said data bus electrically coupled to the emitter;

wherein said switch-transistor electrically isolates a said data bus from a said power supply in a first said second mode of operation and electrically connects said data bus to said power supply in a second said first mode of operation.

12. (canceled)

13. (currently amended) The apparatus of claim 11 wherein the data bus is connected to the switch-transistor via a resistor.

14. (currently amended) The apparatus of claim 13 wherein the resistor is located within a device a subsystem.

15. (currently amended) The apparatus of claim 14 wherein the device said subsystem is an integrated circuit.

16. (canceled)

17. (currently amended) A television signal processing apparatus having a first mode of operation and a second mode of operation comprising:

a data bus;

a first device-subsystem operative in a said first mode of operation and;

a second device-subsystem operative in said first mode of operation and a said second mode of operation;

a control signal active in said first mode; and

a transistor, responsive to said control signal, wherein said first device-subsystem and said second subsystem device are both connected to at least one said data bus line;

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wherein said data bus ~~line~~ is connected to a first power supply via a first resistor integrated within said first ~~device-subsystem~~ and said data bus ~~line~~ is connected to a second power supply via a second resistor integrated within said second ~~device-subsystem~~; and

wherein said first resistor is electrically isolated from said first power supply during said second mode of operation and electrically connected to said first power supply during said ~~second-first~~ mode of operation by said transistor.

18. (canceled)

19. (canceled)

20. (canceled)

21. (canceled)

22. (canceled)

23. (currently amended) The television signal processing apparatus of claim 17 wherein said first ~~device-subsystem~~ is an integrated circuit.

24. (currently amended) The television signal processing apparatus of claim 17 wherein said second ~~device-subsystem~~ is an integrated circuit.